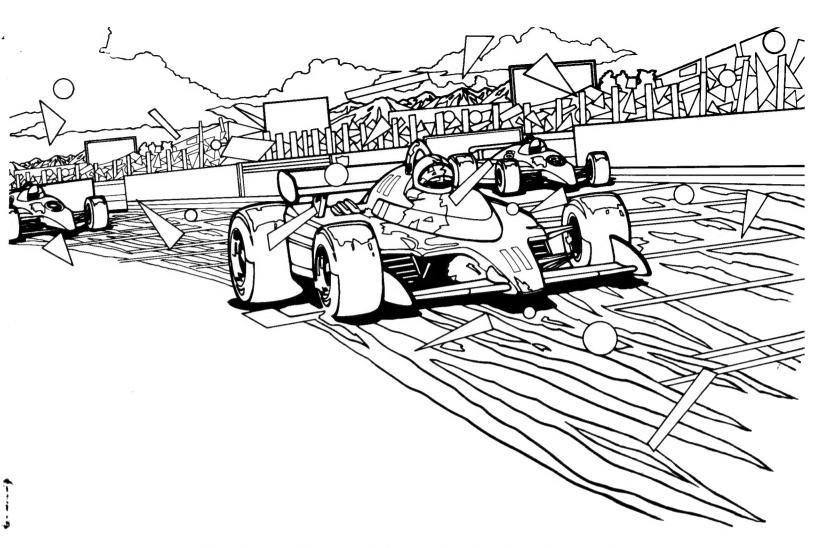
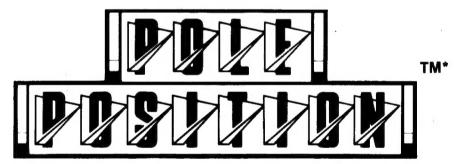
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Schematic Package Supplement to

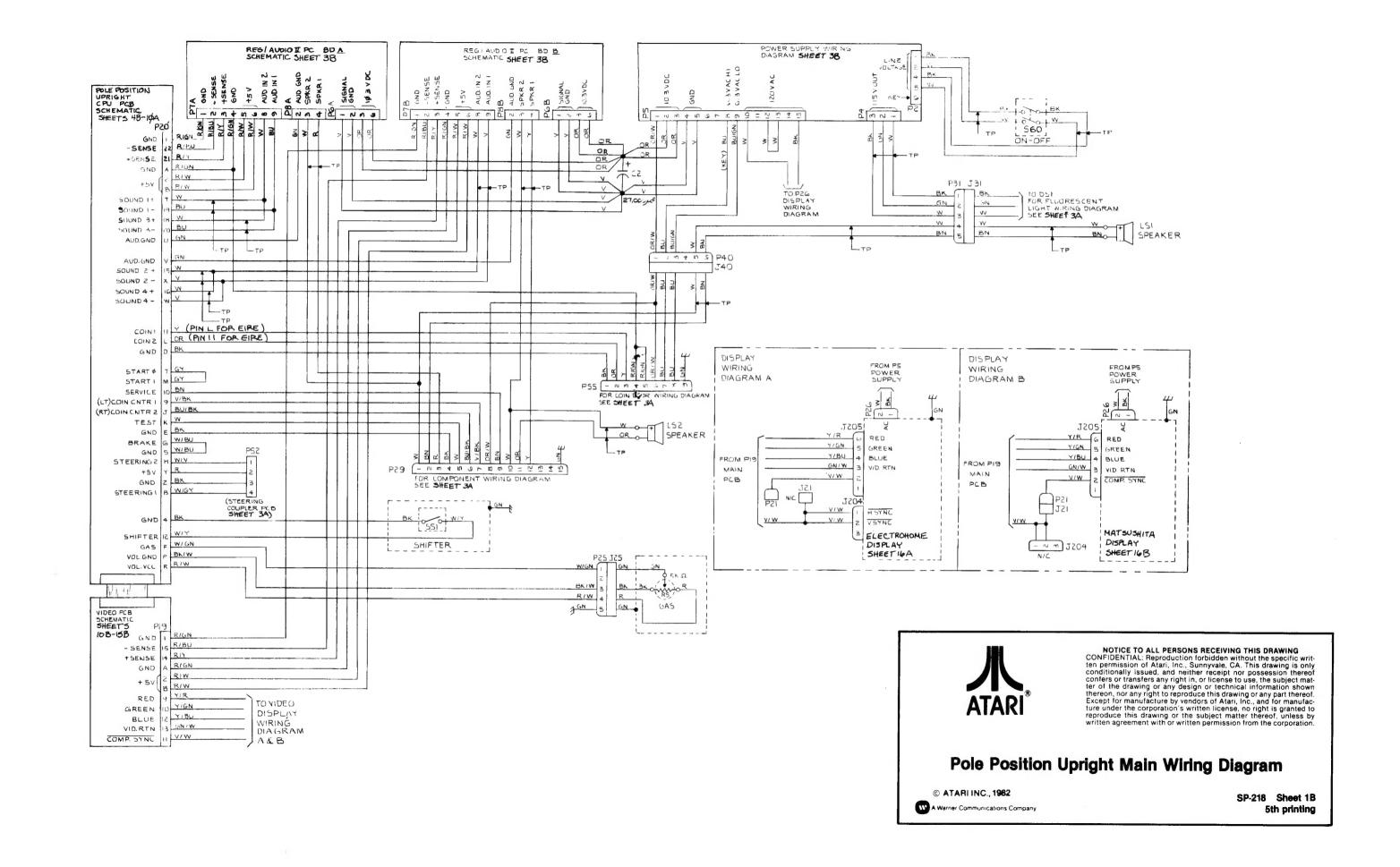


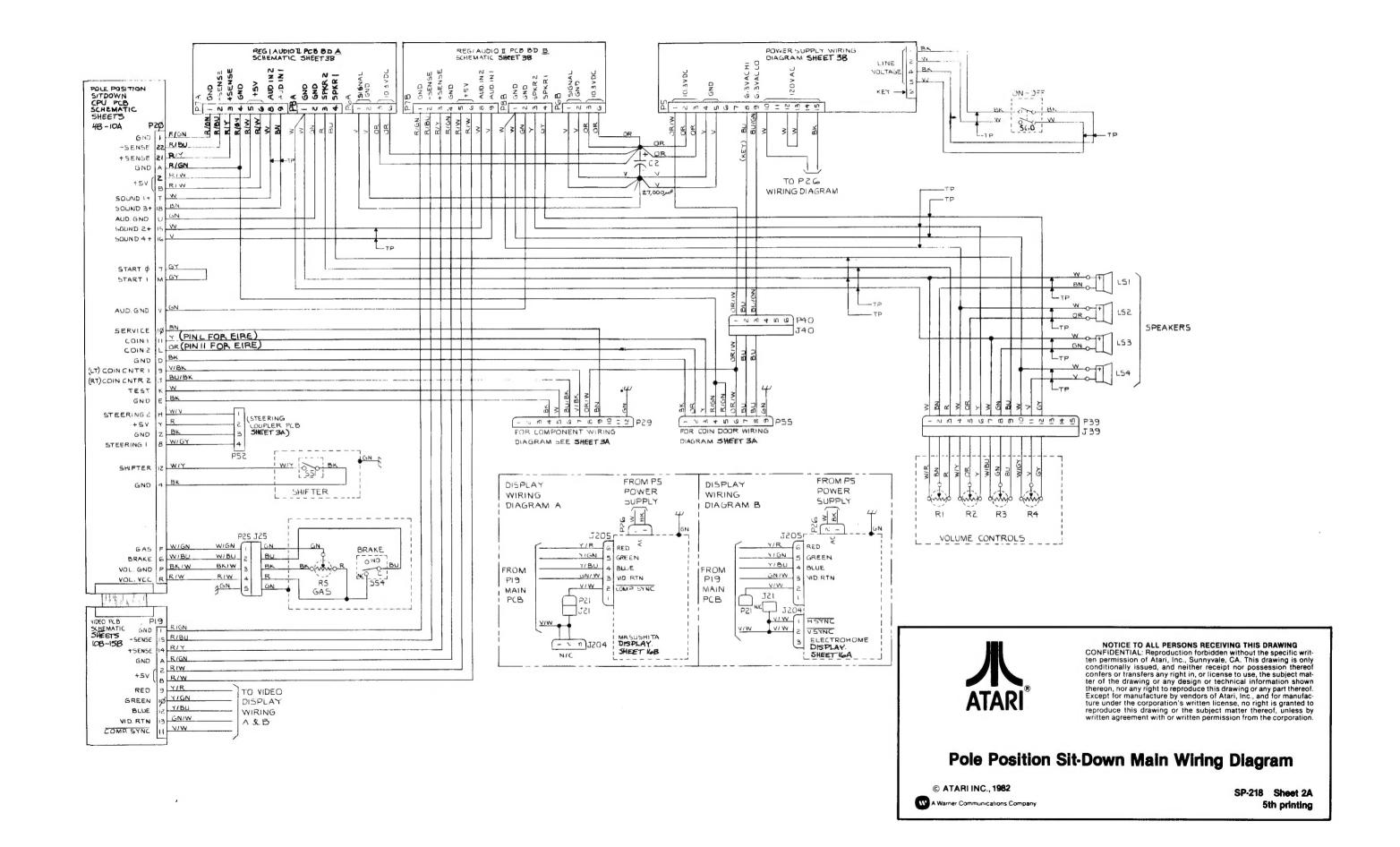
Operation, Maintenance, and Service Manual

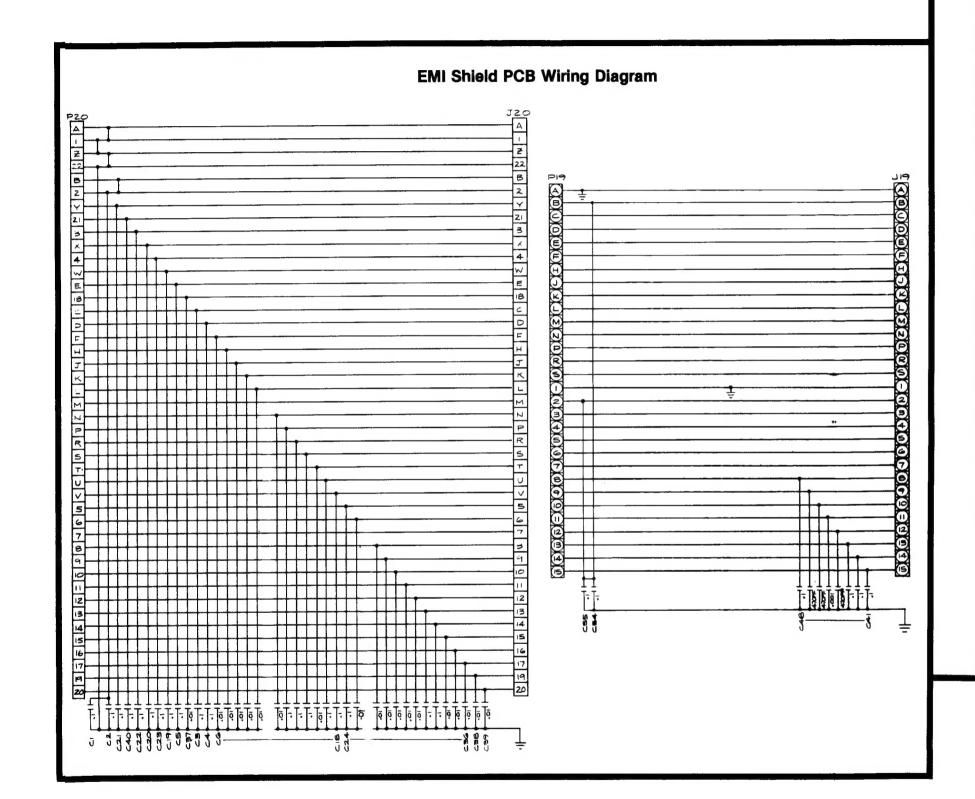
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2M

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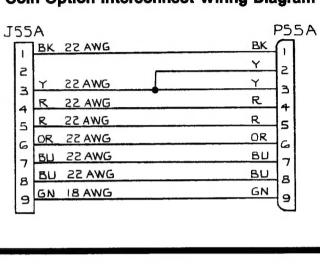




EMI End PCB Wiring Diagram



Coin Option Interconnect Wiring Diagram





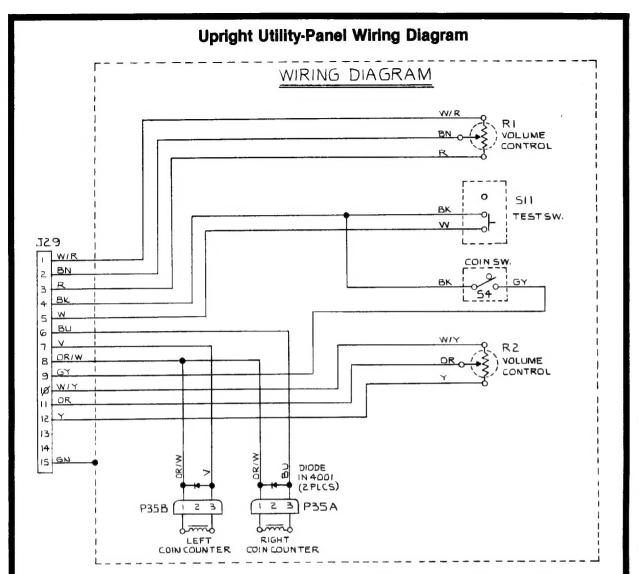
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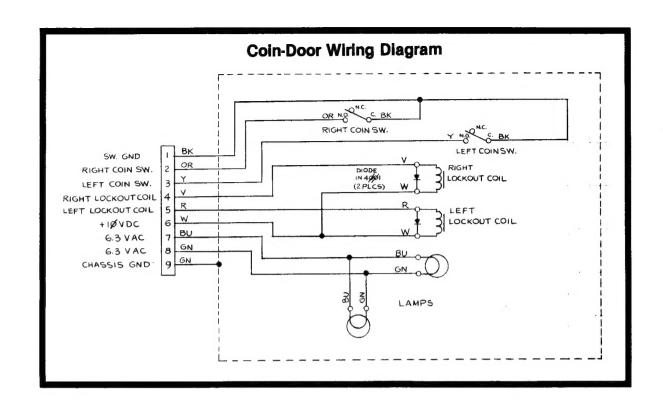
Pole Position Game Wiring Interfaces

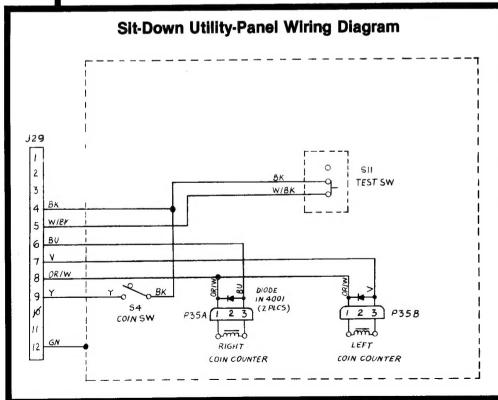
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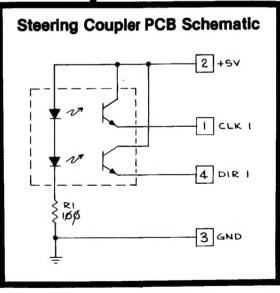
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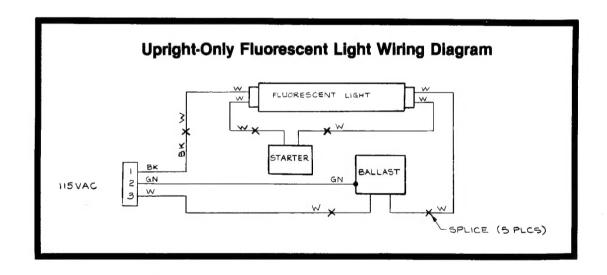








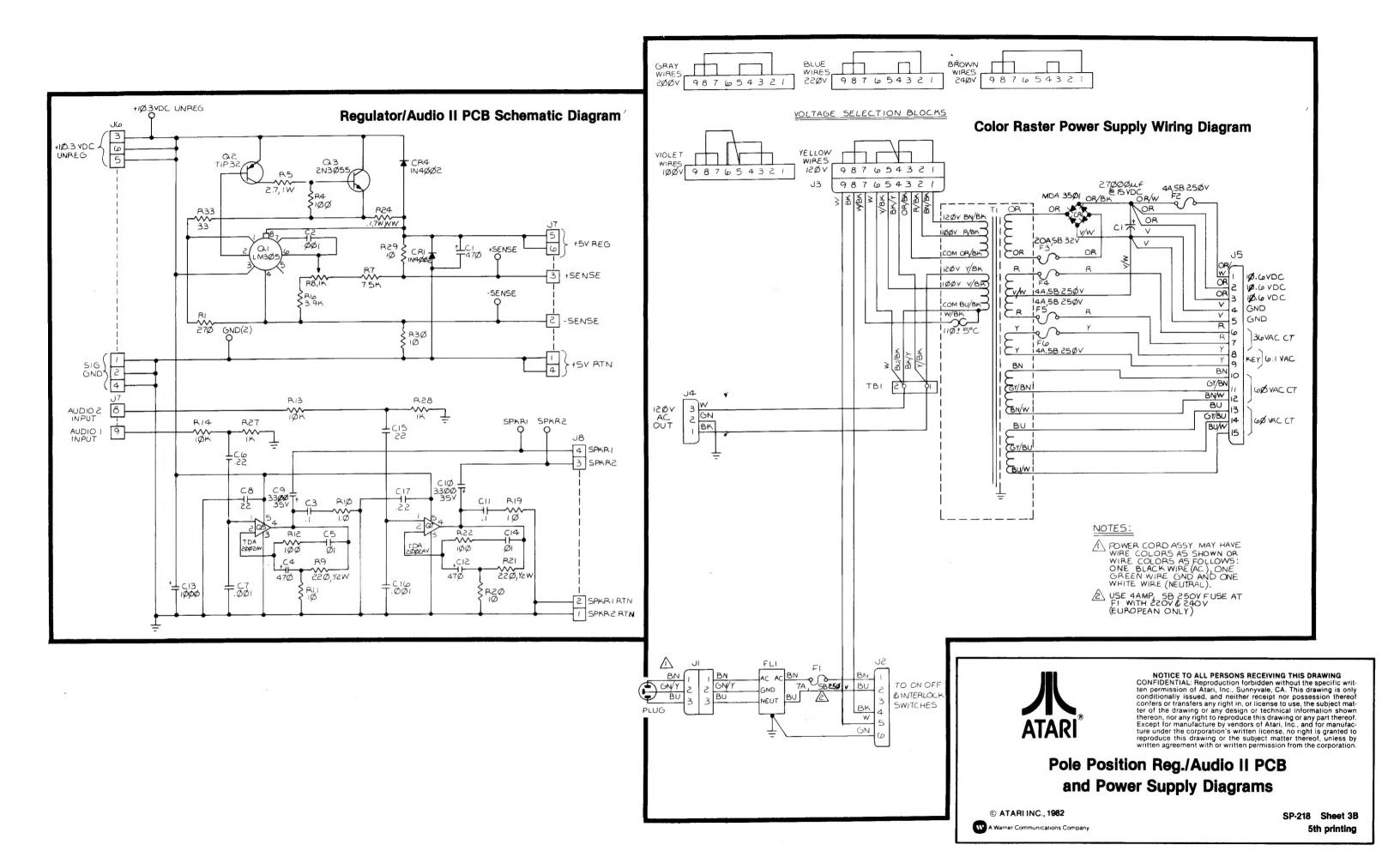






Pole Position Game Wiring Interfaces

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MEMORY MAP CPU 1 AND 2

HEXADECIMAL ADDRESS	READ/ WRITE	FUNCTION
0000-37FFF	R	Program Memory
4000-7FFF	R	Program Memory
8000-8FFF (8700-87FF) (8F00-8FFF)	R/W	Motion Object Memory Vertical and Horizontal Position Character, Color, Vertical Size, Horizontal Size
9000-97FF (9000-93FF) (9700-97FF	R/W	Road Memory Character Horizontal Scroll
9800-9FFF	R/W	Alphanumeric Memory
A000-AFFF)	R/W	View Character Memory
C000	W	View Horizontal Position
C100	W	Road Vertical Position

CPU₃

HEXADECIMAL ADDRESS	READ/ WRITE	FUNCTION
0000-1FFF 2000-2FFF 3000-37FF	R R R/W	Program Memory Program Memory Battery Back-Up RAM
4000-43FF (4380-43FF)	R/W	Motion Object Memory Vertical and Horizontal Position
4400-47FF (4780-47FF)	R/W	Motion Object Memory Character, Color, Vertical Size, Horizontal Size
4800-48FF (4800-49FF) (4B80-4BFF	R/W	Road Memory Character Horizontal Scroll
4C00-57FF (4C00-4FFF) (5000-53FF)	R/W	Alphanumeric Memory Alphanumeric View Character
8000-83FF (83C0-83FF)	R/W	Sound Memory Sound
9000 A000	R/W R/W	4-Bit CPU Controller Input/Output
A000	W R	IRQ Enable (1 = enable, 0 = disable) Bit 0: Not Used Bit 1: 128 V Bit 2: Power-Line Sense Bit 3: ADC End Flag
A001 A002 A003 A004 A005 A006 S007	W W W W W W	4-Bit CPU Enable Sound Enable ADC Input Select CPU 1 Enable CPU 2 Enable Start Switch Color Enable
A100 A200 A300	W W W	Watchdog Reset Car Sound (Lower Nybble) Car Sound (Upper Nybble)

Schematic Reference Designators and Symbols

Logic symbols depict the logic function performed by that particular device and may differ from the manufacturer's data.

REFERENCE DESIGNATORS:		WIRE COLORS:	
C CR F J	Capacitor Diode, signal or rectifier Fuse Connector	R GN Y W	Red Green Yellow White
L LS P Q	Inductor, fixed or variable Speaker Connector Transistor or silicon-controlled rectifier	BU BN BK OR	Blue Brown Black Orange Violet
R S T TP	Resistor, fixed or variable Switch Transformer Twisted wire pair	ĞY	Gray
VR Y	Voltage regulator Crystal		

Electrical components shown on the schematic diagrams are in the following units unless otherwise noted:

Capacitors = microfarads (μ f) Resistors = ohms (Ω) Inductors = microhenrys (μ h)

SYMBOLS:





Test Point

PCB edge connector pad



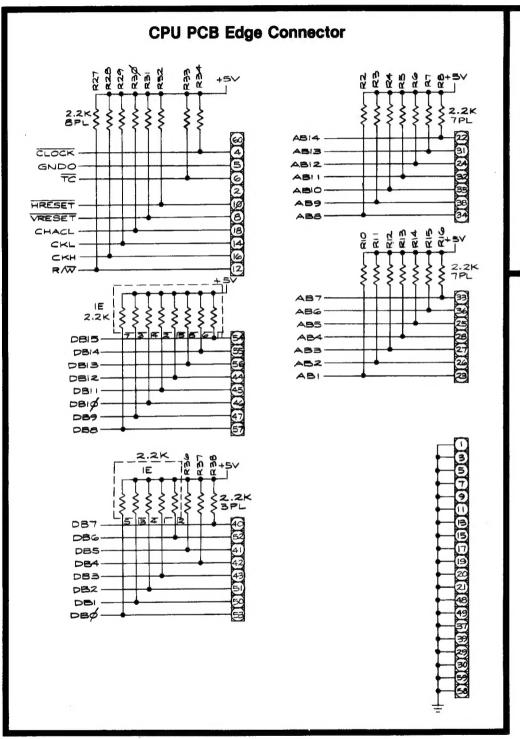
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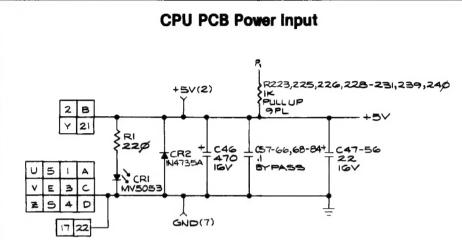
Pole Position Memory Map and Schematic Notes

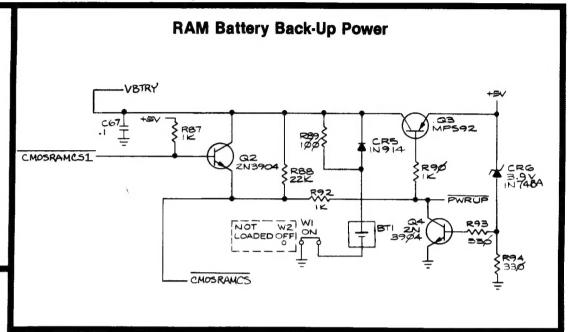
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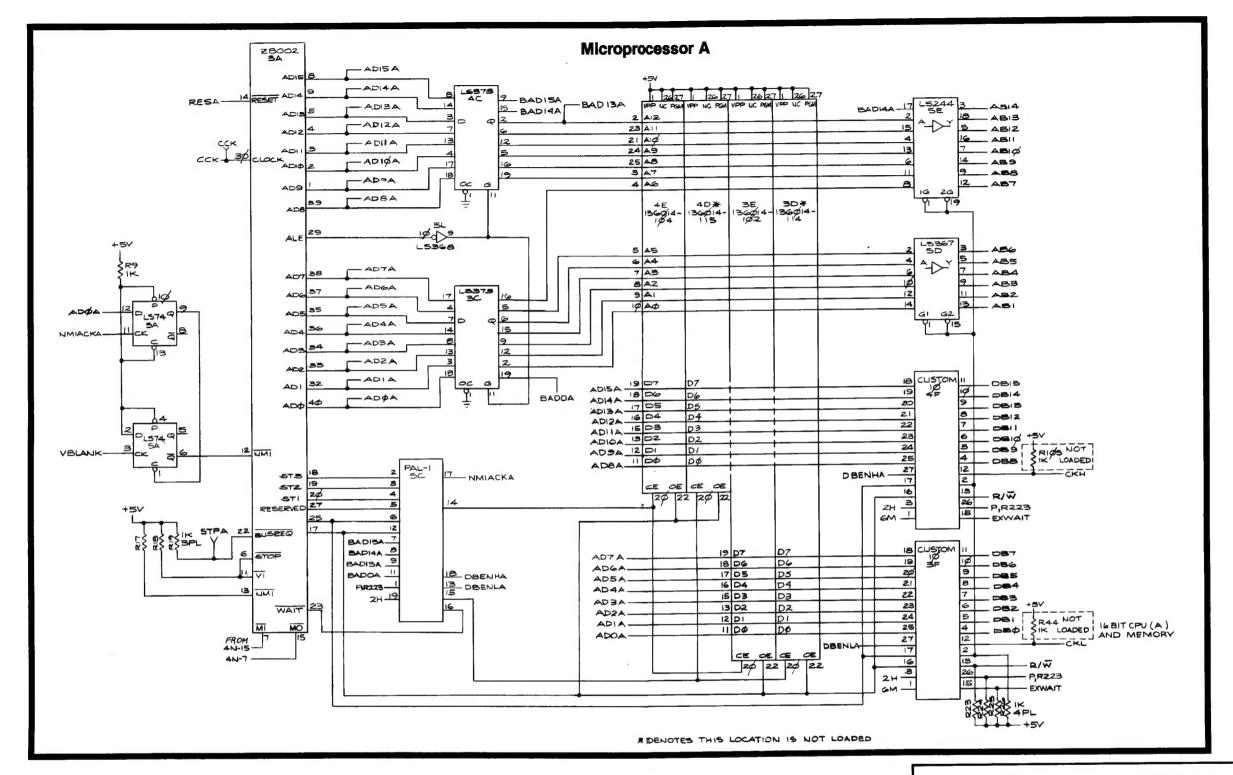


Pole Position CPU PCB Schematic Diagram

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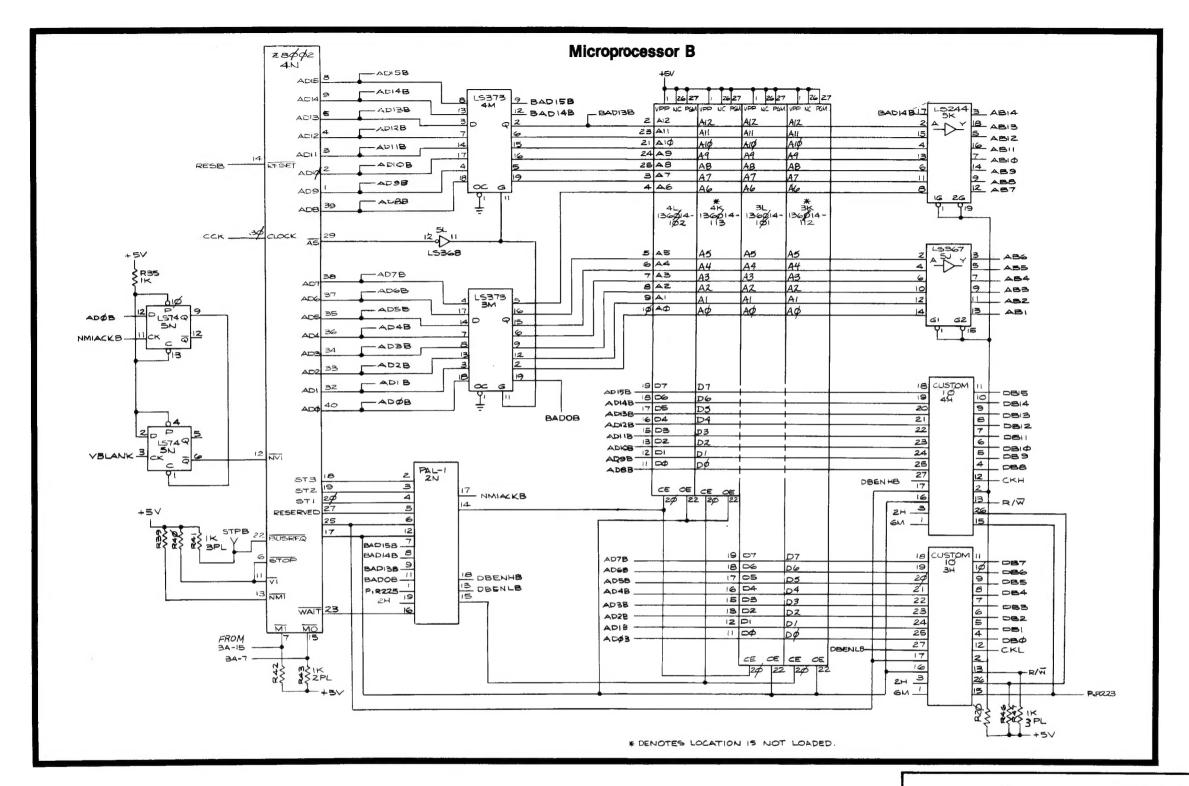
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Pole Position CPU PCB Schematic Diagram

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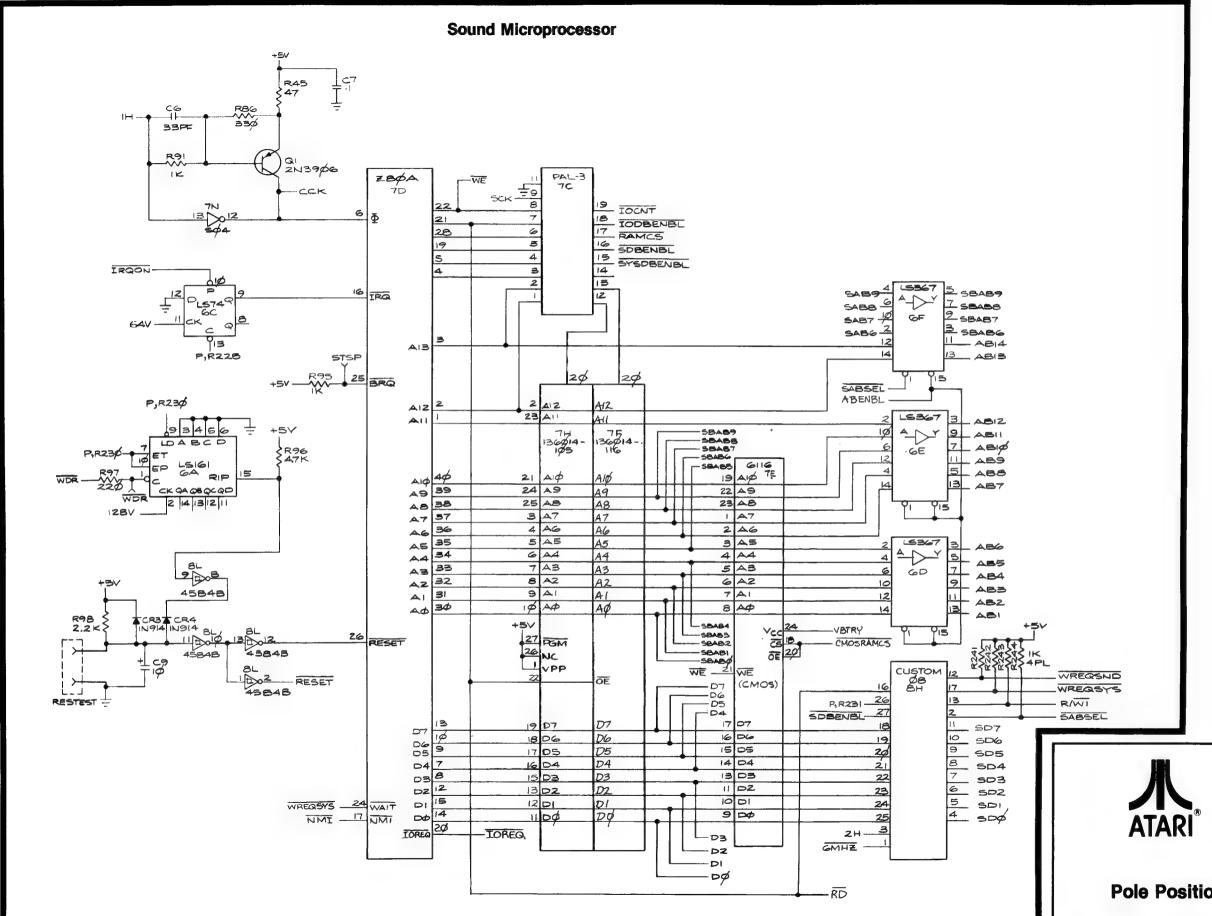


Pole Position CPU PCB Schematic Diagram

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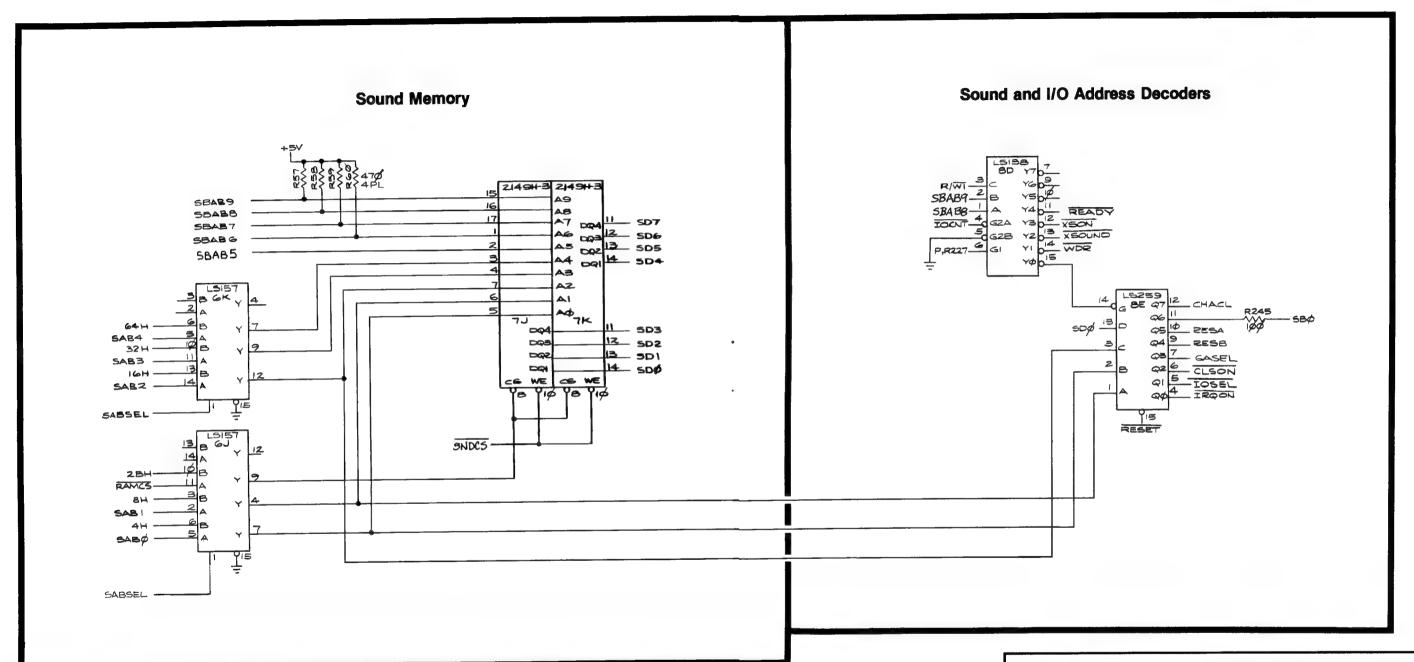


Pole Position CPU PCB Schematic Diagram

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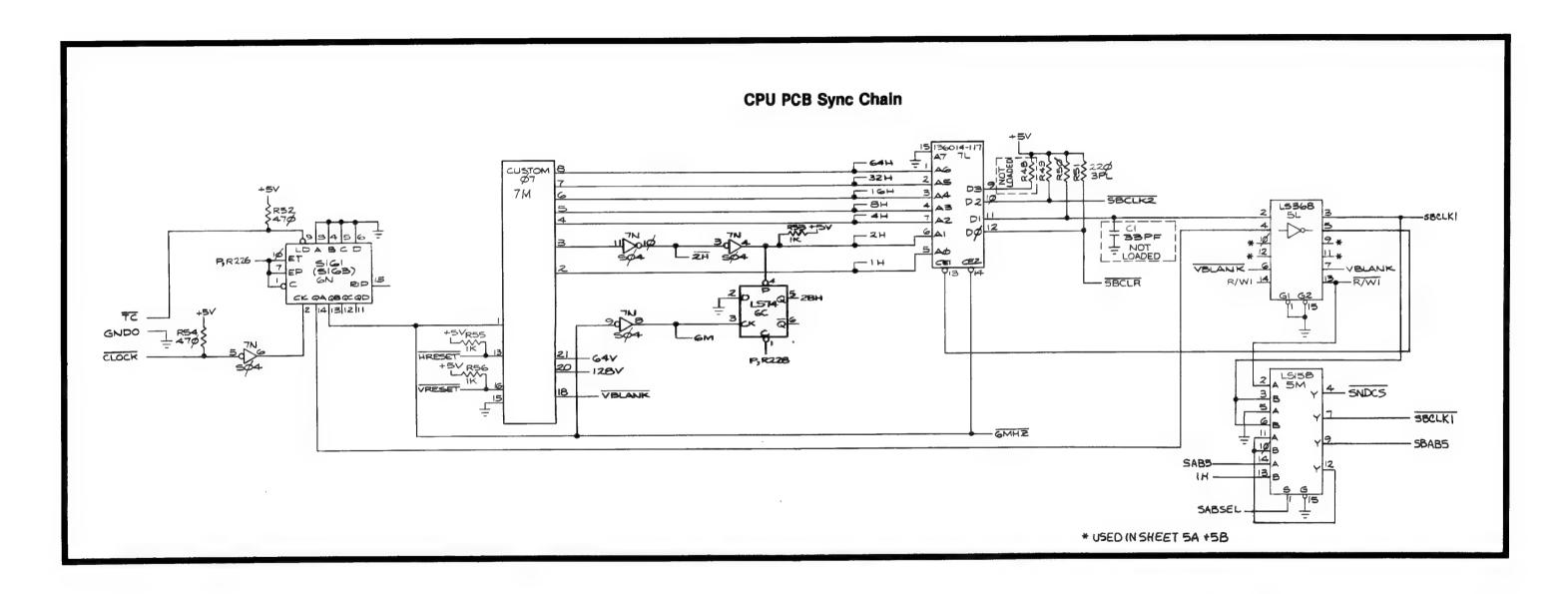


Pole Position CPU PCB Schematic Diagram

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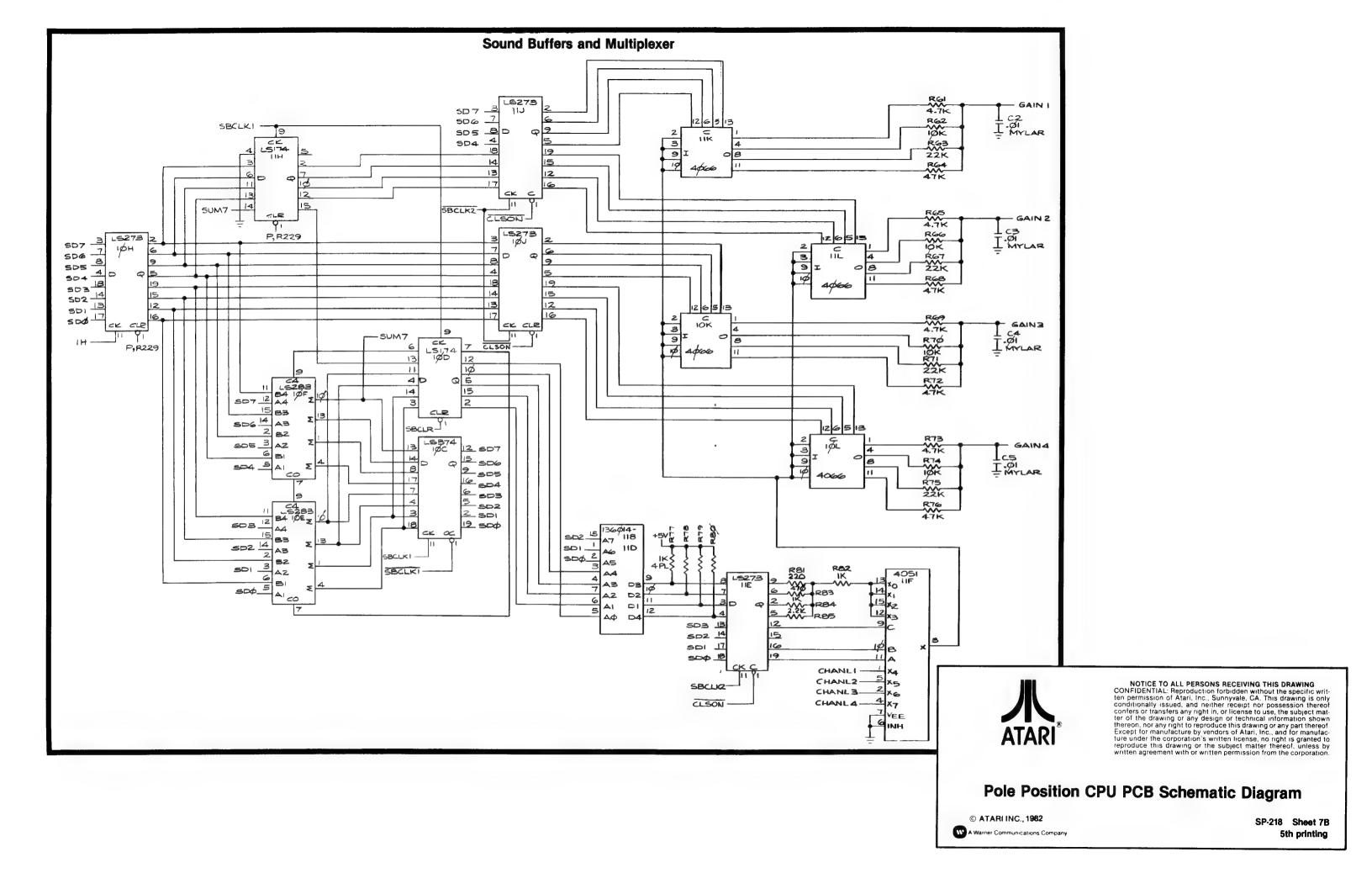
SP-218 Sheet 6B 5th printing

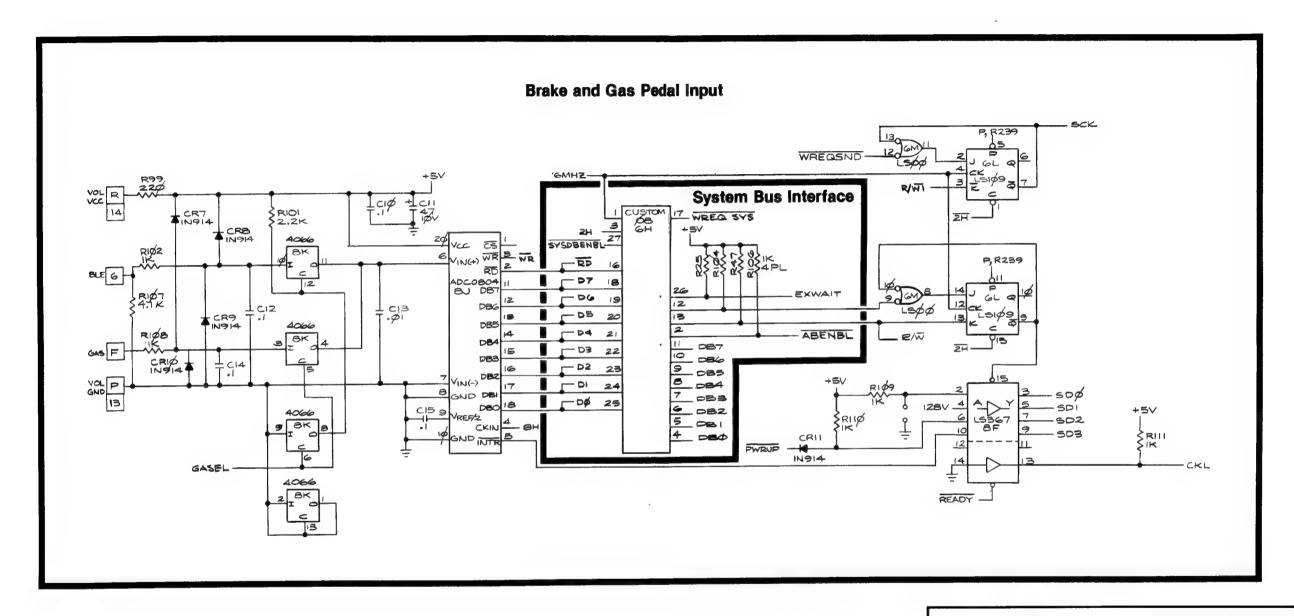




Pole Position CPU PCB Schematic Diagram

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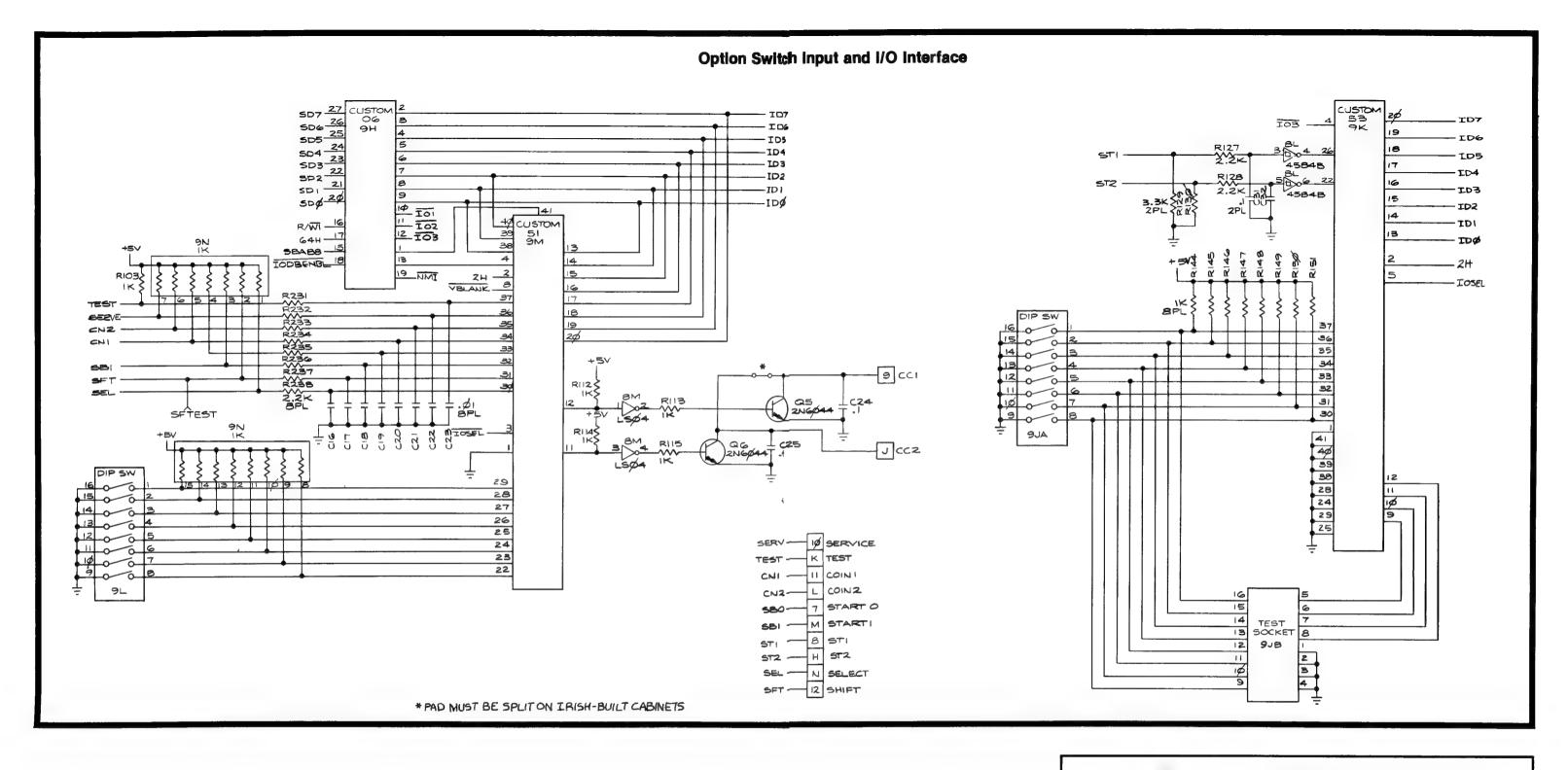




Pole Position CPU PCB Schematic Diagram

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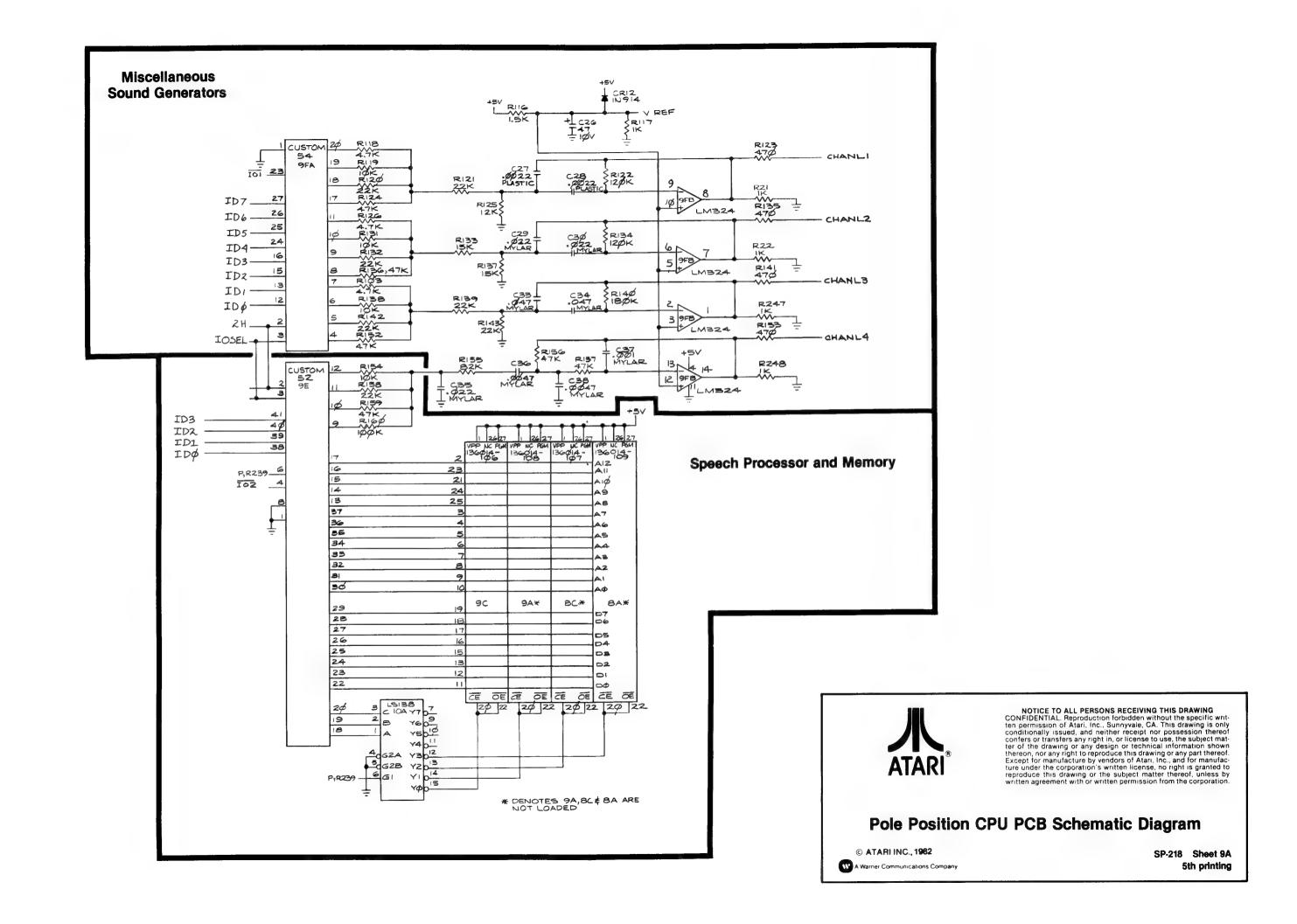


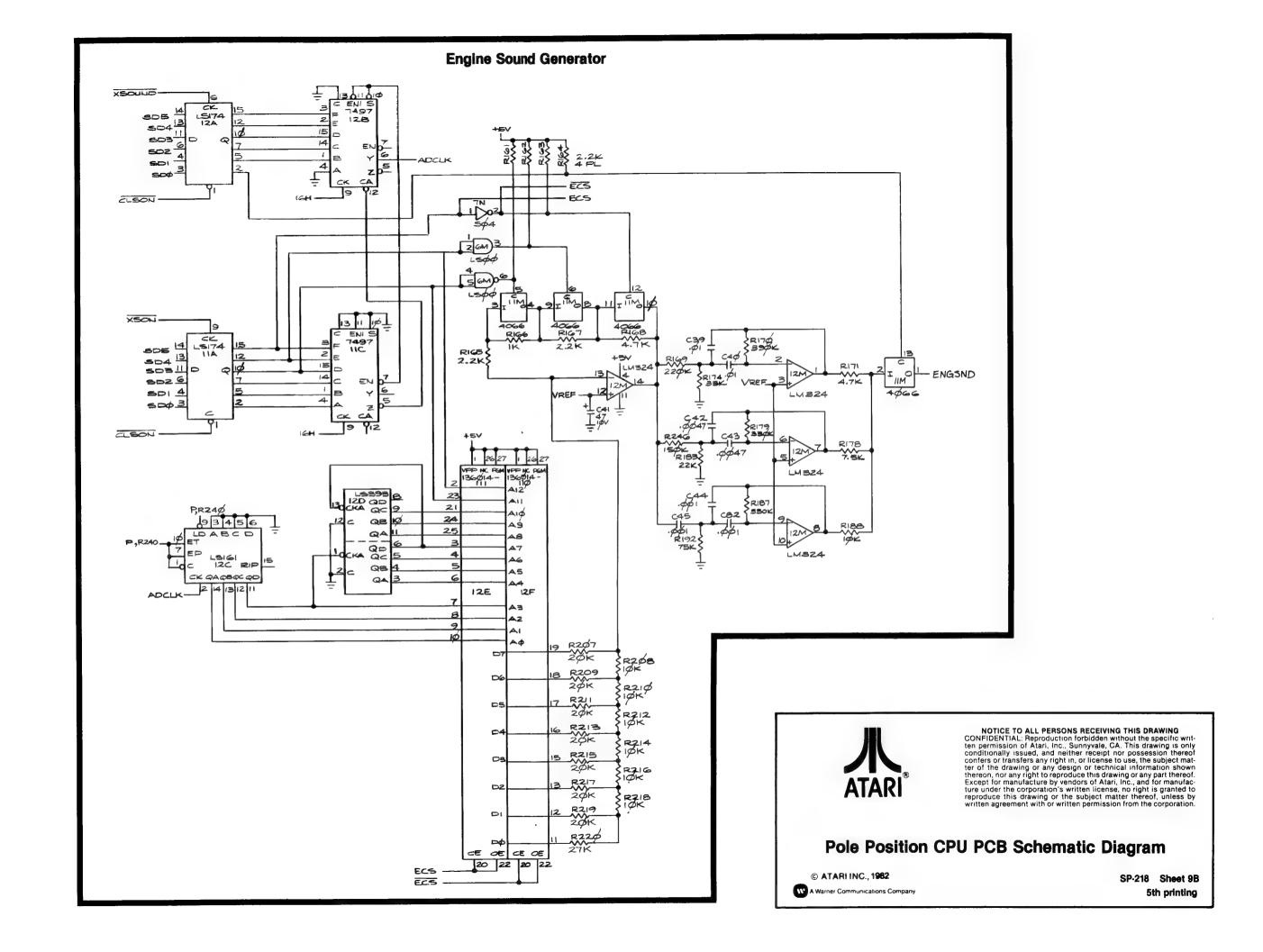
Pole Position CPU PCB Schematic Diagram

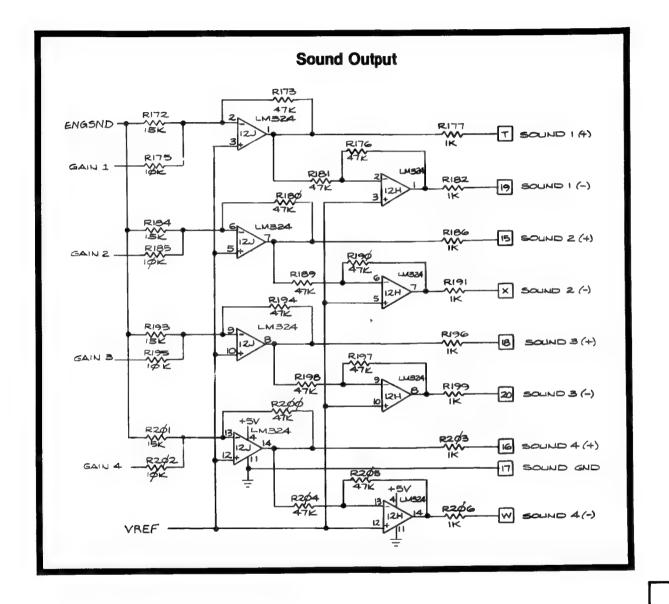
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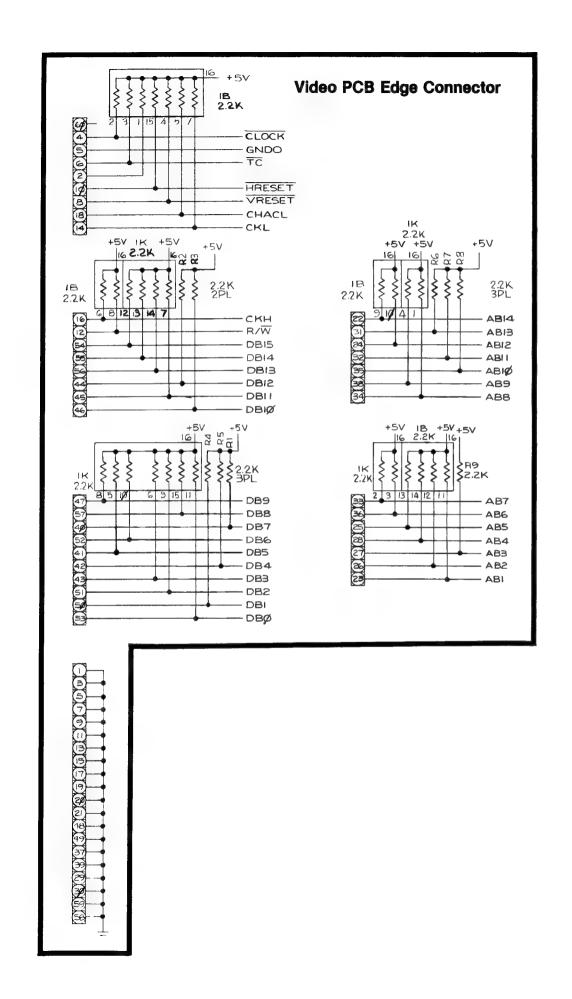


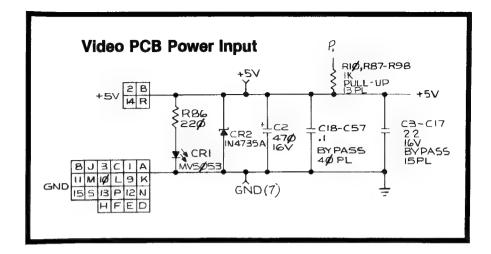


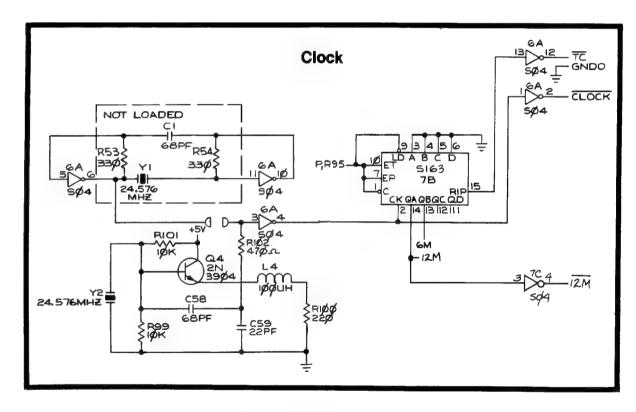


Pole Position CPU PCB Schematic Diagram

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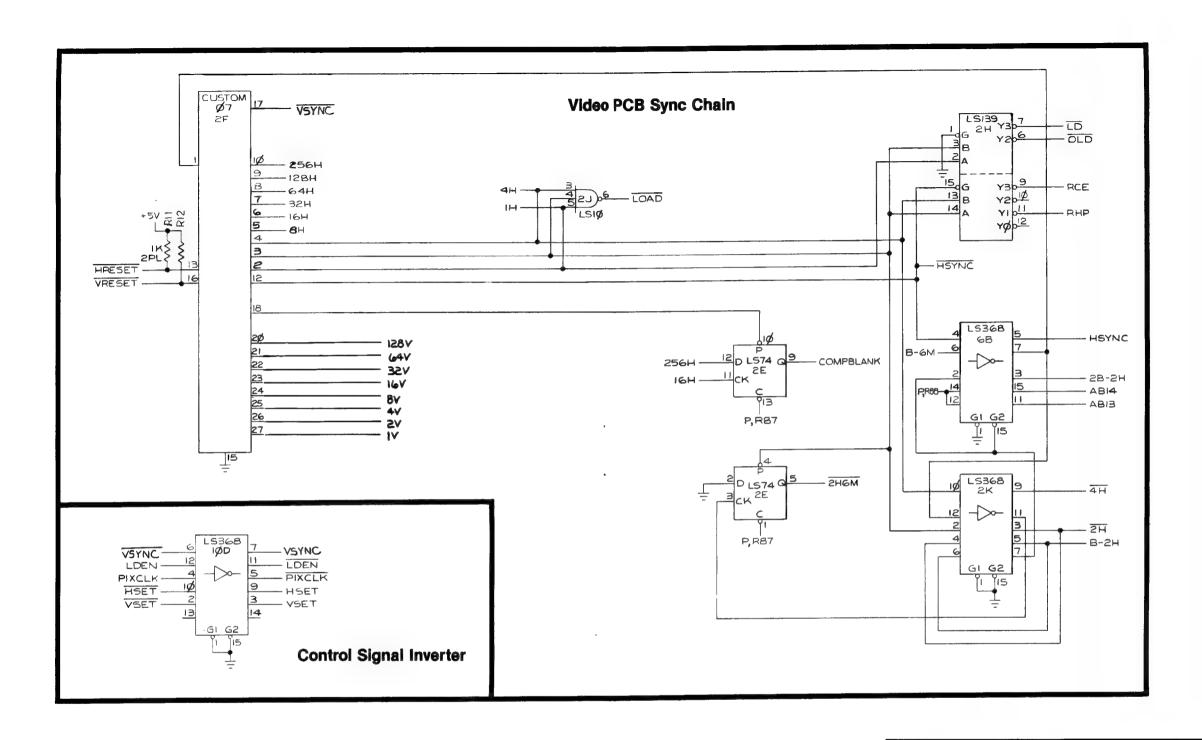


Pole Position Video PCB Schematic Diagram

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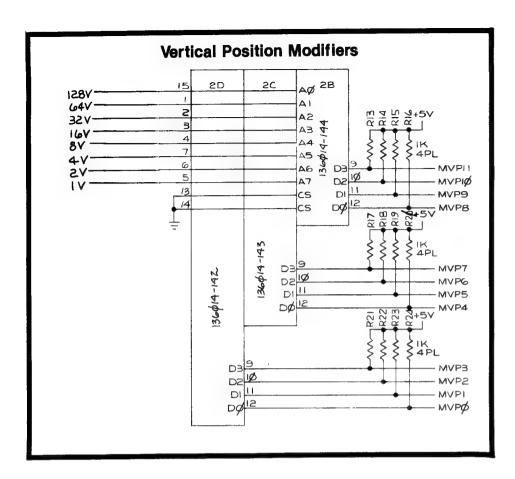


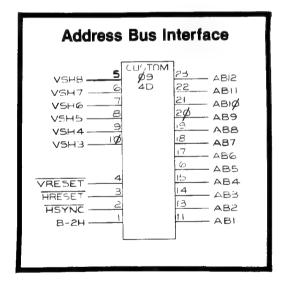
Pole Position Video PCB Schematic Diagram

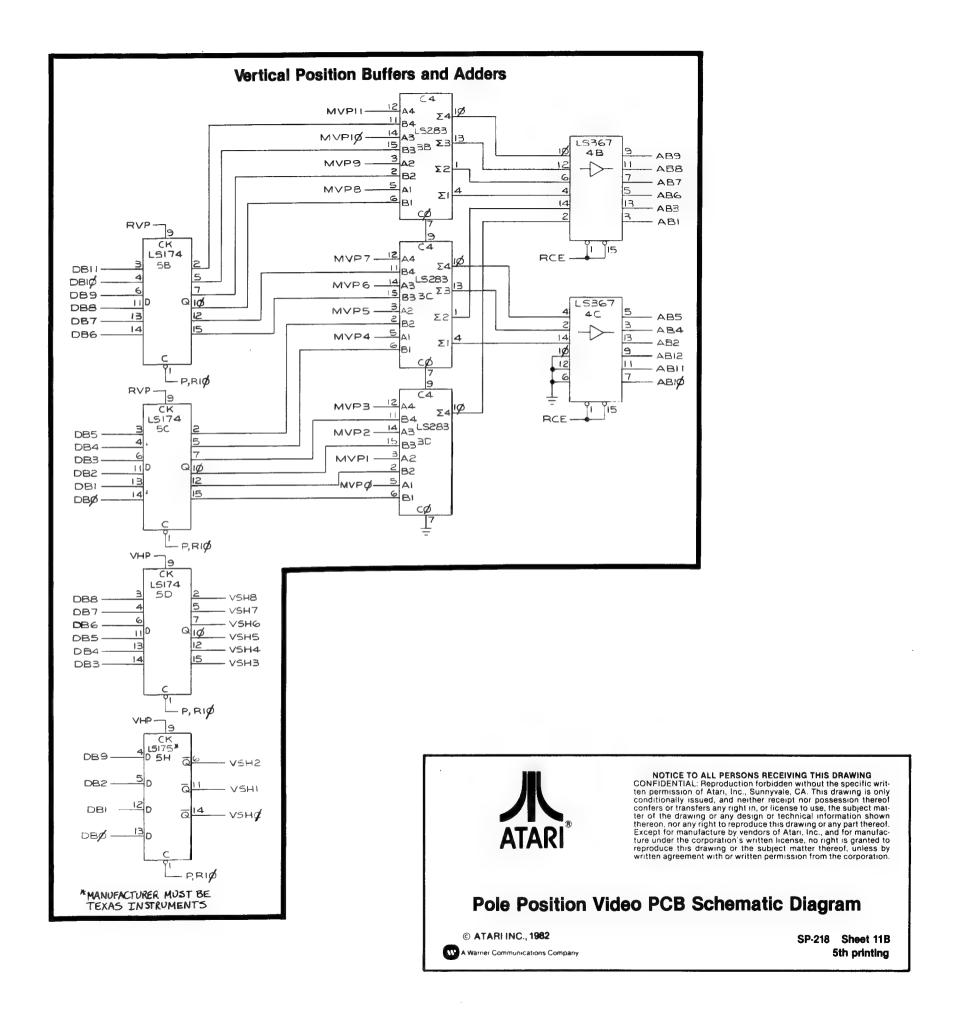
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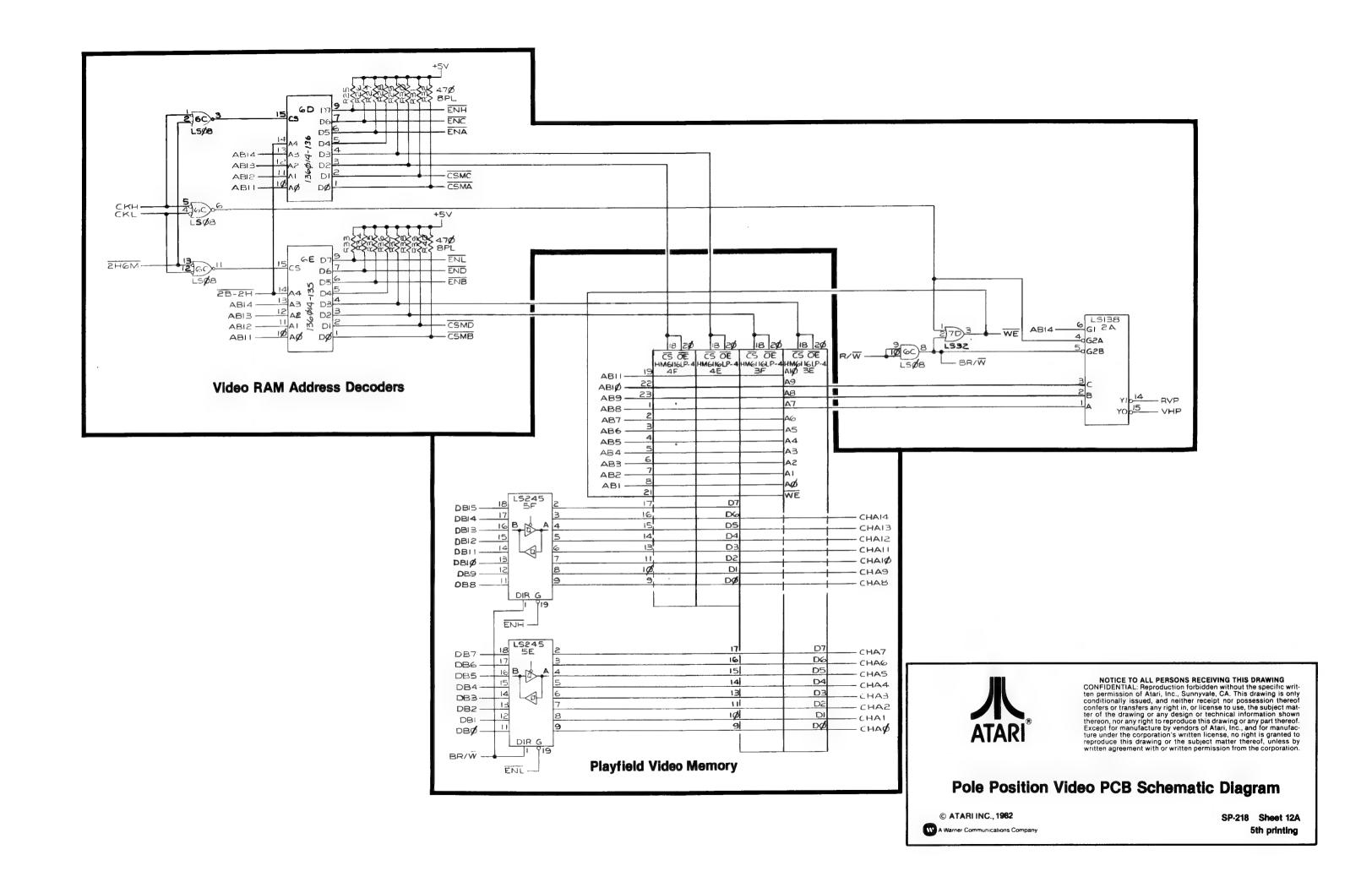
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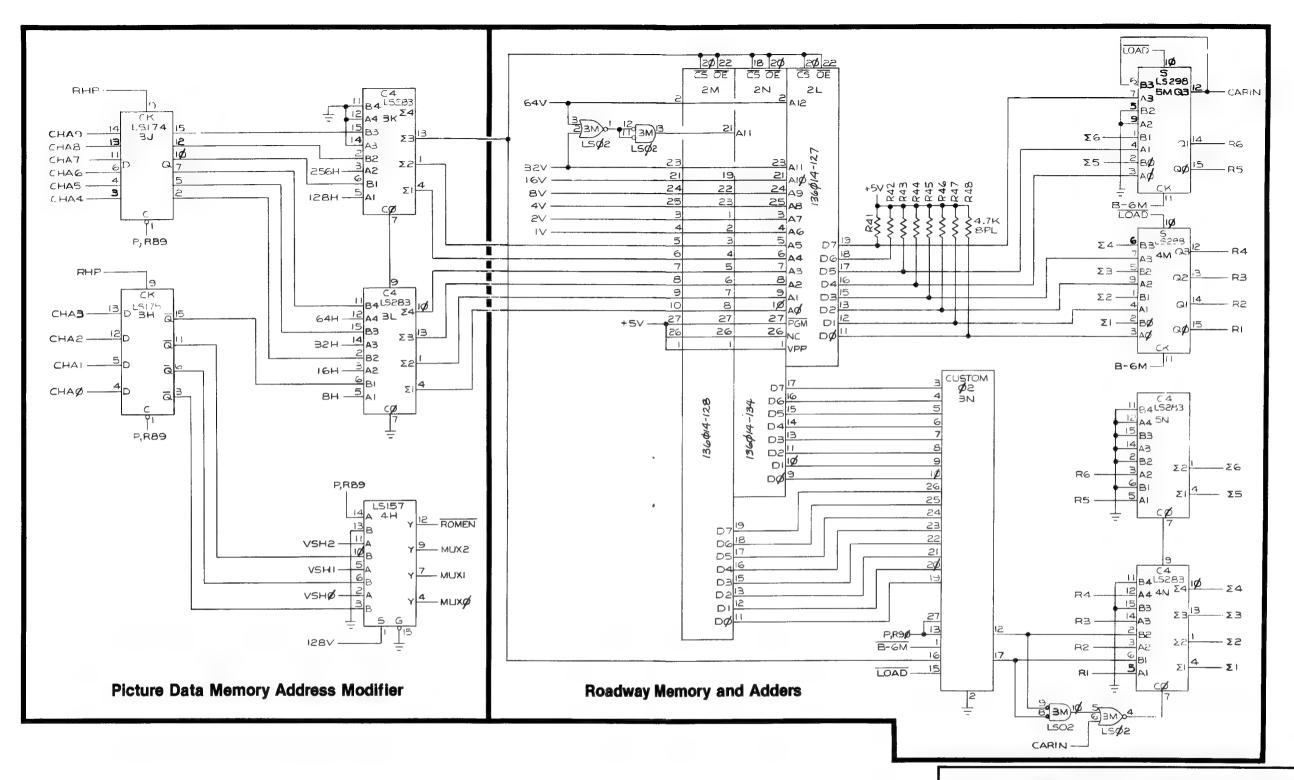
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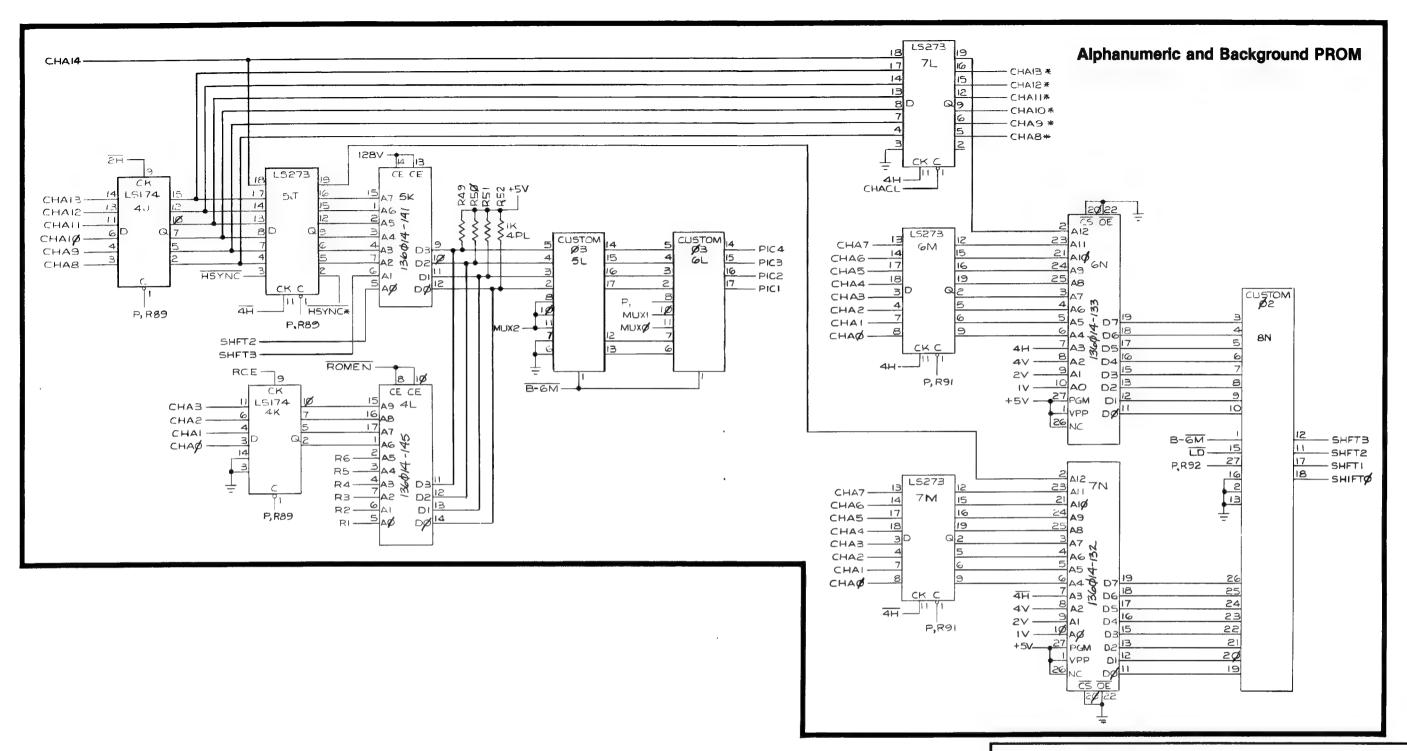


Pole Position Video PCB Schematic Diagram

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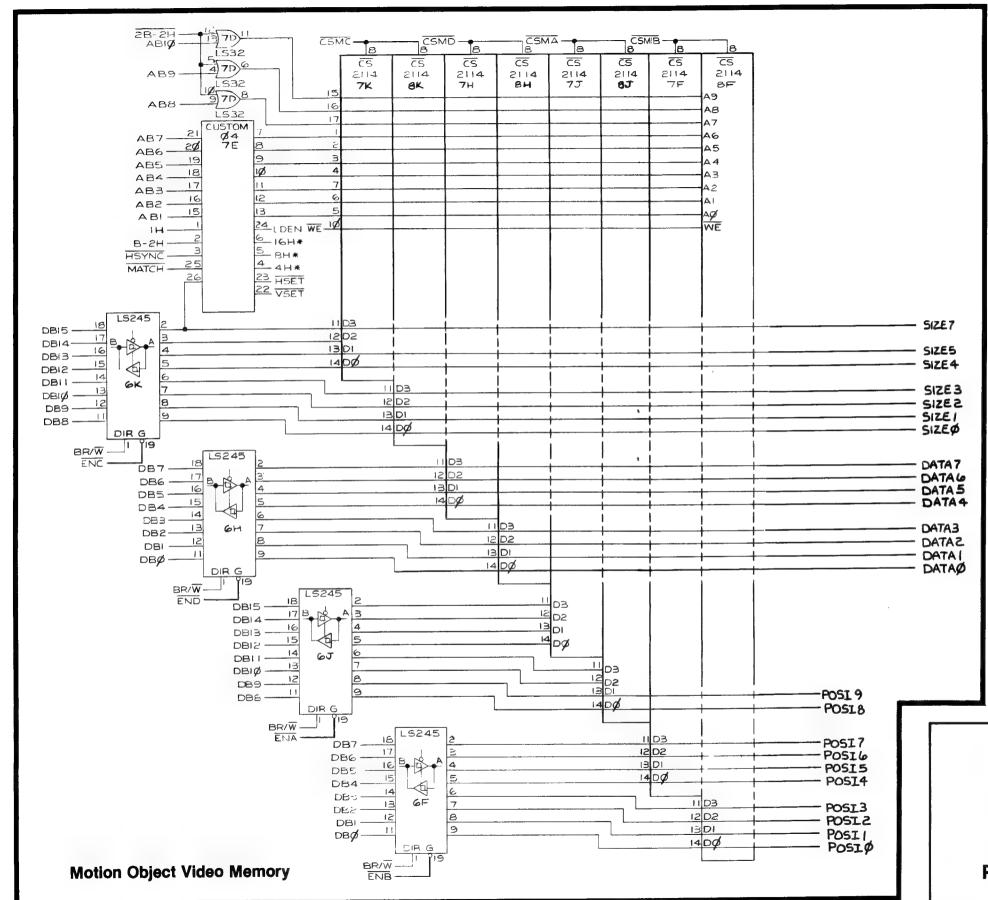




Pole Position Video PCB Schematic Diagram

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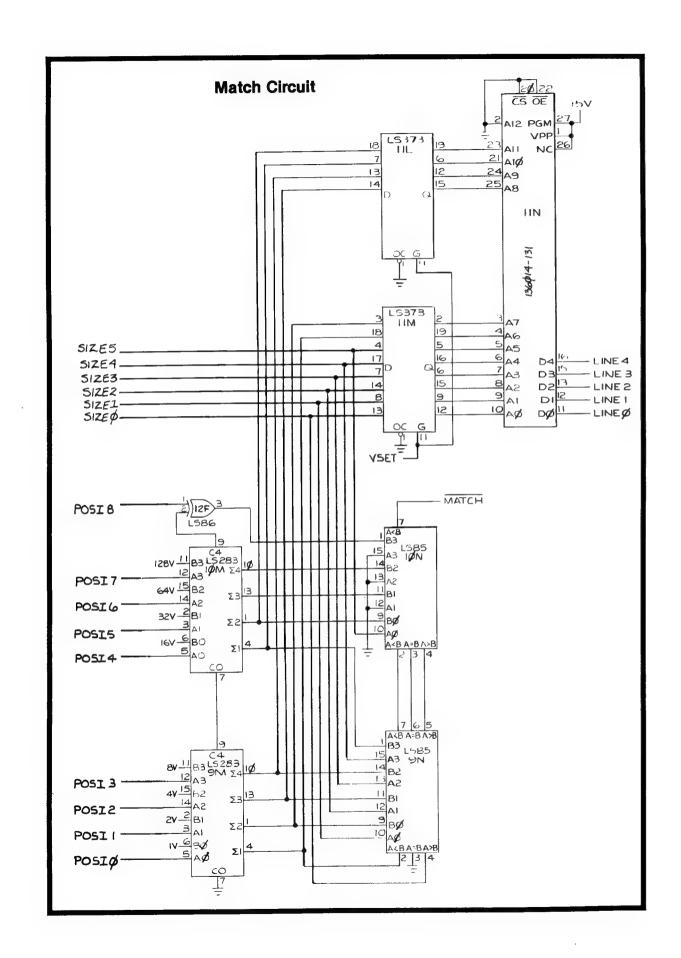


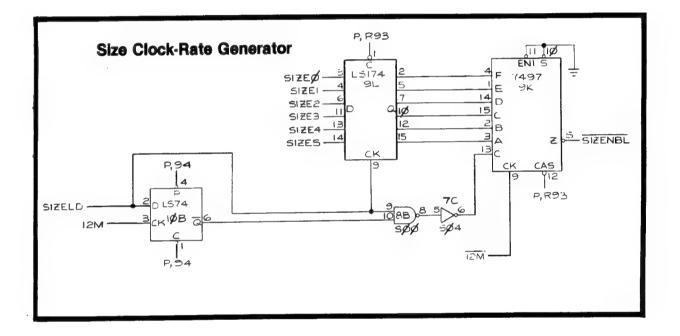


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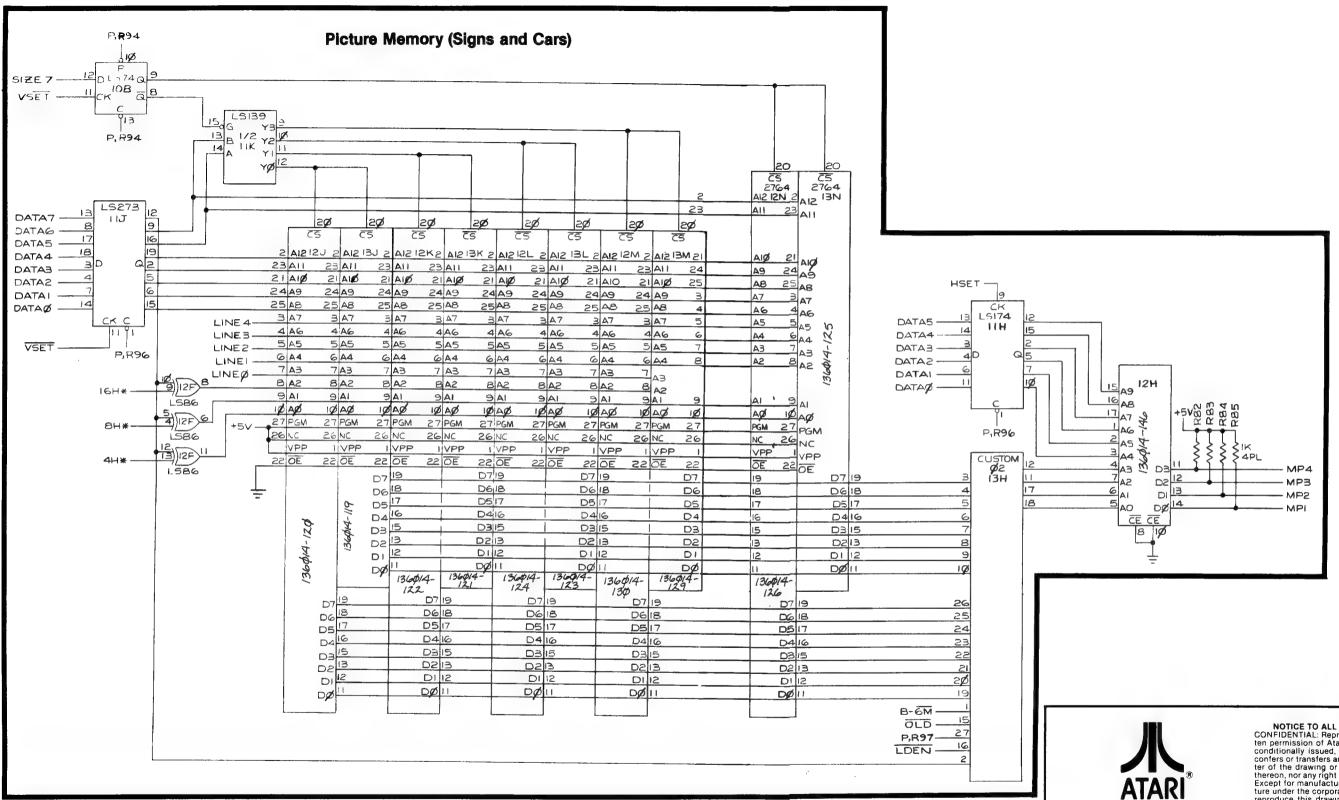




Pole Position Video PCB Schematic Diagram

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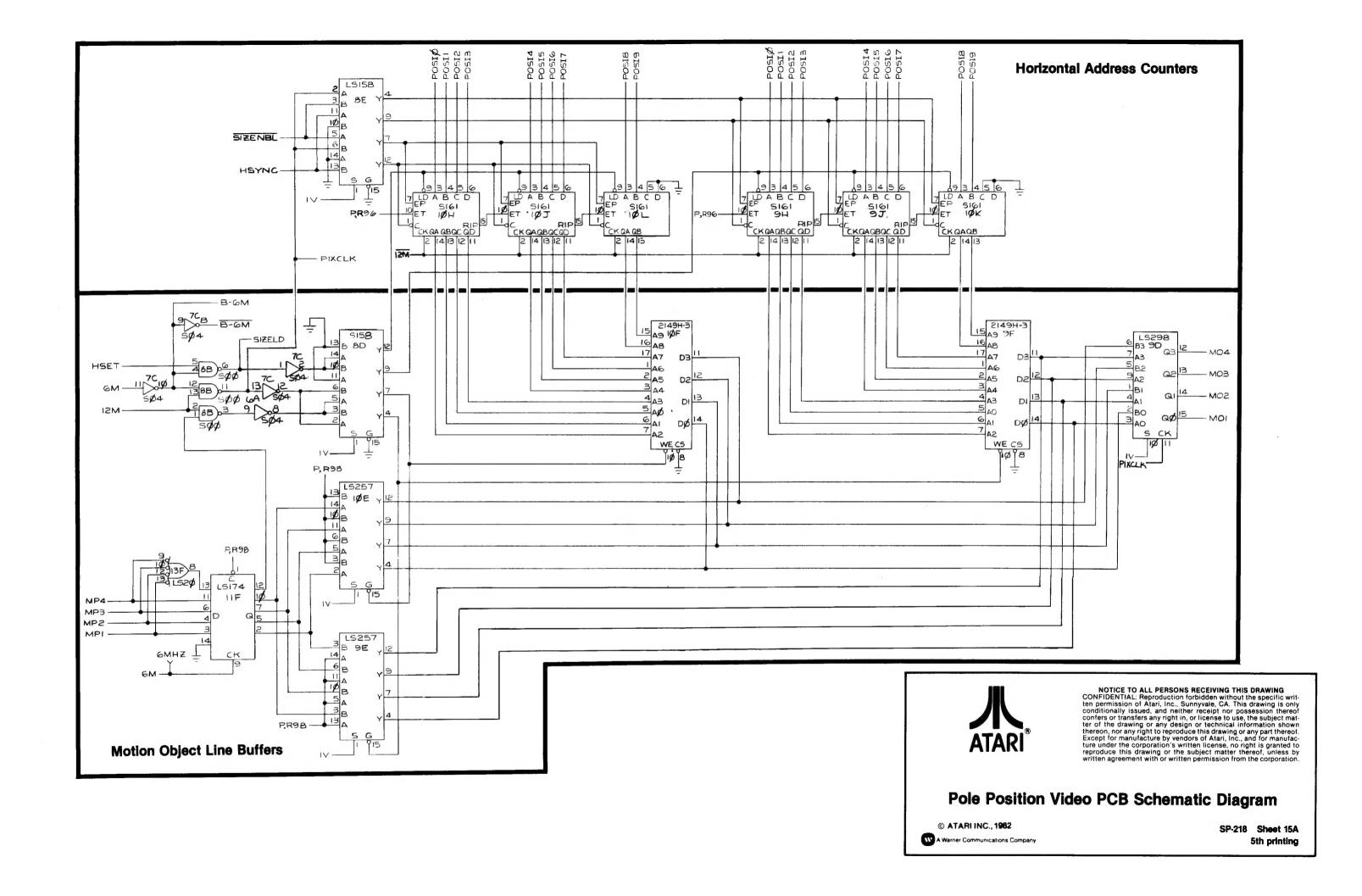


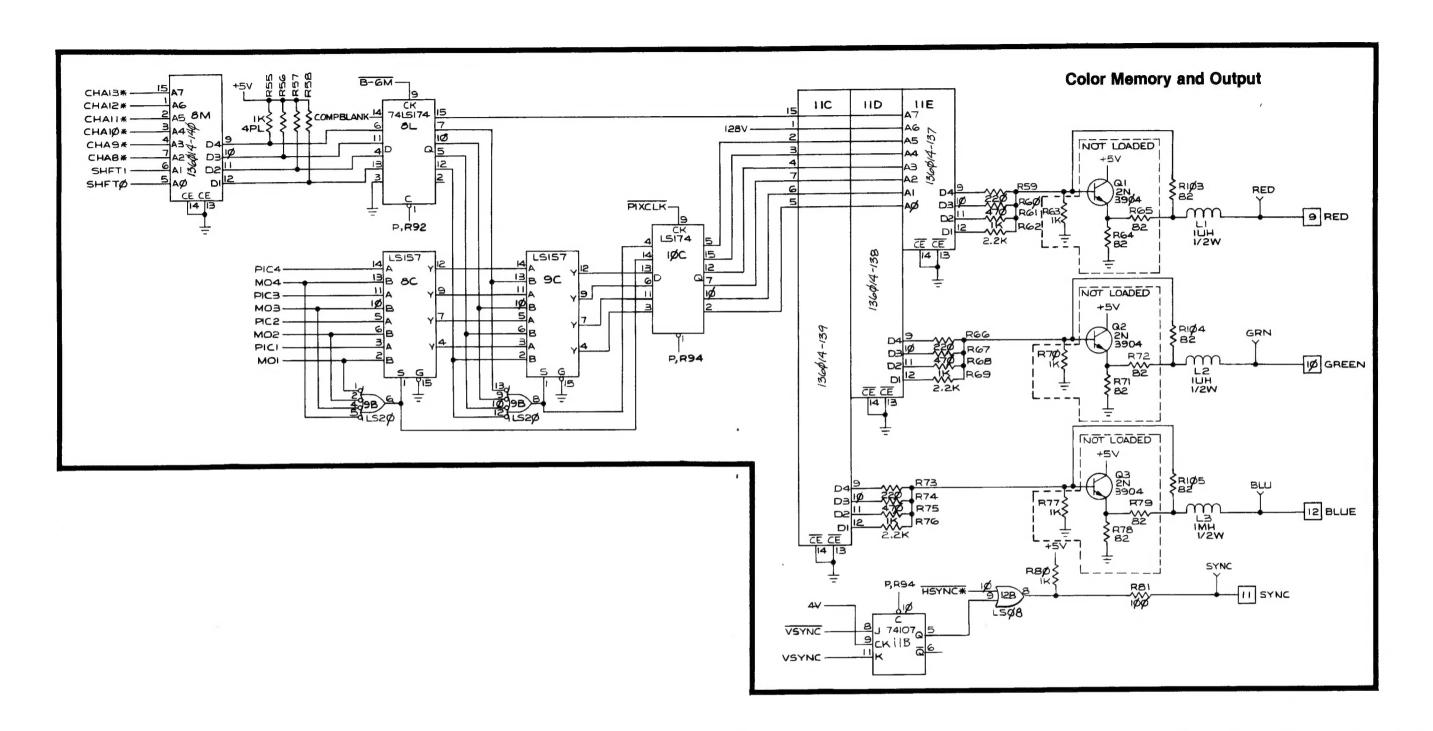
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Pole Position Video PCB Schematic Diagram

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